

Patent Abstracts of Japan

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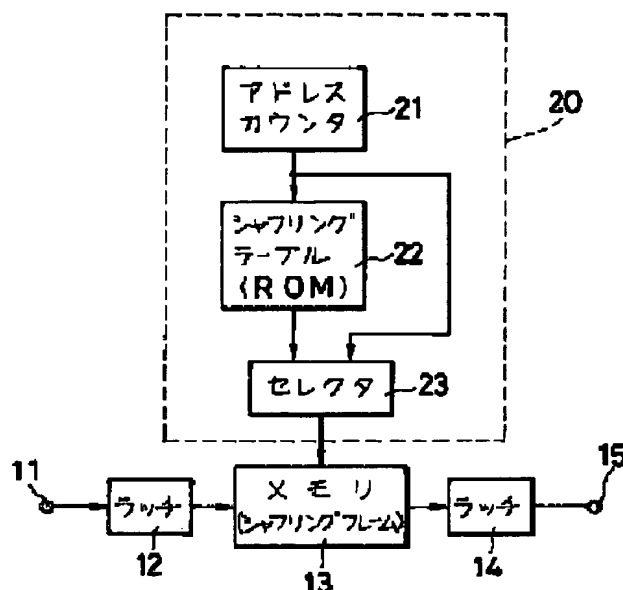
APPLICATION DATE : 26-11-91
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TITLE : SHUFFLING CIRCUIT



ABSTRACT : PURPOSE: To halve a memory capacity for a shuffling, reduce cost and power consumption.

CONSTITUTION: By writing the input data from an input terminal 11 into a memory 13 and reading, the output data, which are shuffled, are taken out of a terminal 15. An address generating circuit 20 switches the output address from an address counter 21 and the address which is taken out this address via a shuffling table 22 at every shuffling frame, which is shuffling process unit, by a selector 23 and the memory 13 is accessed. At the memory 13, the input data are written into the same address, from which data are read.

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